
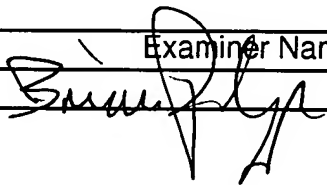
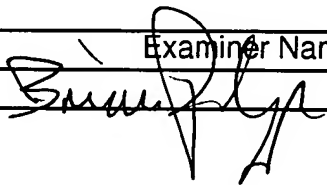
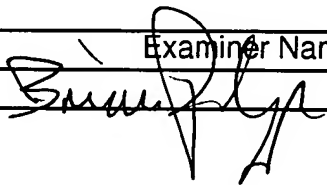


ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

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Title of Invention	Video Signal Processing Apparatus and Integrated Circuit																																		
<p>Application Number : 10/650078 </p> <p>Confirmation Number: 7216</p> <p>First Named Applicant: Kazunori Chida</p> <p>Attorney Docket Number: 16359-002001</p> <p>Art Unit: 2614</p> <p>Examiner:</p> <p>Search string: (6380979 or 5365274).pn</p> <p>US Patent Documents</p> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td>1</td><td>1</td><td>6380979</td><td>2002-04-30</td><td>Tokoi et al.</td><td></td><td></td><td></td></tr><tr><td>2</td><td>2</td><td>5365274</td><td>1994-11-15</td><td>Seki et al.</td><td></td><td></td><td></td></tr></tbody></table> <p>Signature</p> <table border="1"><thead><tr><th>Examiner Name</th><th>Date</th></tr></thead><tbody><tr><td></td><td>13 Apr 06</td></tr></tbody></table>								init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass	1	1	6380979	2002-04-30	Tokoi et al.				2	2	5365274	1994-11-15	Seki et al.				Examiner Name	Date		13 Apr 06
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